



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 7512

SERIAL NUMBER 10/763,255	FILING DATE 01/26/2004  RULE	CLASS 714	GROUP ART UNIT 2138	ATTORNEY DOCKET NO. R2180.0188/P188
-----------------------------	---------------------------------------	--------------	------------------------	---

## APPLICANTS

Takamitsu Yamada, Kanagawa-Ken, JAPAN;

 Yasutaka Tsukamoto, Kanagawa-Ken, JAPAN;  
 Hidetaka Minami, Kanagawa-Ken, JAPAN;
\*\* CONTINUING DATA \*\*\*\*\* *none pme*
 \*\* FOREIGN APPLICATIONS \*\*\*\*\* *yes pme*  
 JAPAN 2003-018920 01/28/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/28/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 6
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>pme</i> Initials				

## ADDRESS

 24998  
 DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP  
 2101 L Street, NW  
 Washington, DC  
 20037

## TITLE

Semiconductor integrated circuit and scan test method therefor

FILING FEE  RECEIVED 1230	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue )
------------------------------------	---	--